IN THE UNITED STATES PATENT AND TRADEMARK OFFICE 09/602.395

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|---|----------------|
| Filing Date | une 22, 2000 |
| Inventor J | |
| Assignee Micron Ted | chnology, Inc. |
| Group Art Unit | |
| Examiner | Pham, T. |
| Attorney's Docket No | MI22-1384 |
| Title: Methods of Forming Oxide Regions Over Semiconductor Substrates | 3 |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: <u>3 - 15-20</u>

By:

Jennifer J. Taylor, Ph.

Reg. No. 48,711

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